



00862.022499

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
:)
TAKAO YONEHARA, ET AL.) Examiner: Thanh T. Nguyen
:)
Application No.: 10/059,171) Group Art Unit: 2813
:)
Filed: January 31, 2002)
:)
For: METHOD OF MANUFACTURING)
A THIN-FILM SEMICONDUCTOR)
DEVICE USED FOR A DISPLAY)
REGION AND PERIPHERAL)
CIRCUIT REGION) September 30, 2004

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

Applicants are in receipt of a Notice of Allowance dated September 7, 2004, in the above-referenced application. The issue fee is due on or before December 7, 2004, and is being paid concurrently herewith.

Applicants respectfully traverse the Reasons for Allowance set forth in the Notice of Allowability on the grounds that:

(1) whereas the Reasons refer to "forming a porous layer on a surface of the semiconductor substrate", Claims 17 through 21 instead recite --forming a porous


separation layer in the semiconductor substrate--;

(2) whereas the Reasons refer to "forming an image display portion on the switching element", the claims instead recite --forming an image display portion on the first region--; and

(3) whereas the Reasons refer to "separating the switching element and a peripheral circuit film from the member together with the image display portion", the claims instead recite --separating the first and second regions from the member together with the image display portion--.

Applicants' undersigned attorney may be reached in our Washington, D.C. office by telephone at (202) 530-1010. All correspondence should continue to be directed to our address given below.

Respectfully submitted,



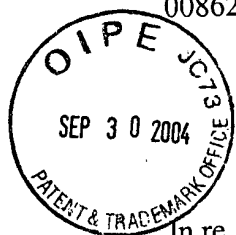
Attorney for Applicants
Damond E. Vadnais
Registration No. 52,310

FITZPATRICK, CELLA, HARPER & SCINTO
30 Rockefeller Plaza
New York, New York 10112-3800
Facsimile: (212) 218-2200
DEV/klm

DC_MAIN 179745v1

PATENT APPLICATION

00862.022499



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TAKAO YONEHARA, ET AL.

Application No.: 10/059,171

Filed: January 31, 2002

For: METHOD OF MANUFACTURING
A THIN-FILM SEMICONDUCTOR
DEVICE USED FOR A DISPLAY
REGION AND PERIPHERAL
CIRCUIT REGION

)
:
) Examiner: Thanh T. Nguyen
:
) Group Art Unit: 2813
:
)
:
)
:
)
:
) September 30, 2004

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

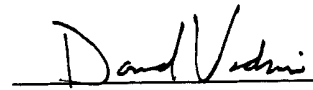
STATEMENT OF SUBSTANCE OF INTERVIEW

Sir:

Applicants wish to thank the Examiner for the courtesies extended to Applicants' undersigned representative during the telephonic interview conducted on August 30, 2004. During the interview, Applicants' undersigned representative authorized the Examiner's Amendment set forth in the September 7, 2004 Notice of Allowability.

Applicants' undersigned attorney may be reached in our Washington, D.C.
office by telephone at (202) 530-1010. All correspondence should continue to be directed to
our below-listed address.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read "Damond Vadnais", is written over a horizontal line.

Attorney for Applicants
Damond E. Vadnais
Registration No. 52,310

FITZPATRICK, CELLA, HARPER & SCINTO
30 Rockefeller Plaza
New York, New York 10112-3801
Facsimile No.: (212) 218-2200
DEV/klm

DC_MAIN 179694v1